

KPCI-3140 Specs

Features Summary

Digital I/O

Port	Lines per port	Type	Interrupt on Bit Change Detection	SSR Drive
A, B, C	8 bidirectional	Level-sensitive	No	Yes
D	8 bidirectional	Level-sensitive	Yes	Yes

Counter/Timers

	Channels	Resolution	Maximum Clock Frequency	External Clock
Counter/Timer	8	16	20MHz	Yes
Interval Timers	4	24	20MHz	No

Digital I/O

	Ports A, B, C	Port D
Number of lines	8 bidirectional per port	8 bidirectional
Inputs		
Input type	Level-sensitive	Level-sensitive
High-level input voltage	2.0V minimum	2.0V minimum
Low-level input voltage	0.8V maximum	0.8V maximum
Minimum pulse width	Not applicable	66ns high and low*
Outputs		
Output driver high voltage	2.4V minimum (IOH = -15mA)	2.4V minimum (IOH = -15mA)
Output driver low voltage	0.5V maximum (IOL = 12mA)	0.5V minimum (IOL = 12mA)

*The minimum pulse width applies only to interrupt-on-change detection. Pulses less than the minimum may not be detected as a change.

Counter/Timer

Number of counter/timer channels	8
Clock Inputs	
Threshold voltage	0.98V
Input sensitivity	±200mV
Input hysteresis	60mV
Input current (Vin = 0)	0
Input current (Vin = 5V)	0.5mA
Minimum pulse width	25ns (high); 25ns (low) sampled by 40MHz
Maximum frequency	19.9MHz typical
Gate Inputs	
High-level input voltage	2.0V minimum
Low-level input voltage	0.8V maximum
Minimum pulse width	25ns (high); 25ns (low) sampled by 40MHz
Counter Outputs	
Output driver high voltage	2.0V minimum (IOH = -15mA); 2.4V minimum (IOH = -3mA)
Output driver low voltage	0.5V maximum (IOL = 24mA); 0.4V maximum (IOL = 12mA)

Power, Physical, and Environmental Specifications

Power	
+5V	405mA + output current nominal
Physical	
Dimensions	6.875 inches (length) by 4.2 inches (width) (PCI short card)
I/O connector	68-pin AMP (#749621-7)
Certification and Compliance	FCC Class A verified; will not compromise FCC compliance of host computer CE
Environmental	
Operating temperature range	0°C to 70°C

Storage temperature range
Relative humidity

-25°C to 85°C
To 95%, noncondensing